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Total Number of Pages in This Submission

Application Number 10/677,596

Filing Date October 2, 2003

First Named Inventor EISENSTADT

Art Unit

Examiner Name

Attorney Docket Number 5853-268

ENCLOSURES (Check all that apply)

- | | | |
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| <input type="checkbox"/> Fee Transmittal Form | <input type="checkbox"/> Drawing(s) | <input type="checkbox"/> After Allowance Communication to a Technology Center (TC) |
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| <input type="checkbox"/> Certified Copy of Priority Document(s) | <input type="checkbox"/> CD, Number of CD(s) _____ | |
| <input type="checkbox"/> Response to Missing Parts/Incomplete Application | <input type="checkbox"/> Remarks | |
| <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53 | No fee is believed due. However, if any fee is due, the Commissioner is authorized to charge any such fee and any additional fees due or credit any overpayment to Deposit Account No. 50-0951. | |

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual	AKERMAN SENTERFITT Neil R. Jetter
Signature	
Date	March 9, 2004

CERTIFICATE OF TRANSMISSION/MAILING

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This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, Washington, DC 20231.

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Application of: EISENSTADT

Serial No.: 10/677,596

Examiner:

Date Filed: October 2, 2003

Group:

For: INTEGRATED POWER SUPPLY CIRCUIT FOR SIMPLIFIED BOARD DESIGN

CERTIFICATE UNDER 37 CFR 1.8(a)

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Neil R. Jetter

Commissioner for Patents
Post Office Box 1450
Alexandria, VA 22313-1450

Date: March 10, 2004

INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to the Duty to Disclose under 37 C.F.R. §1.56, Applicant hereby discloses information that may be relevant to the Examiner's consideration of the above-identified application and the patentability of its claims.

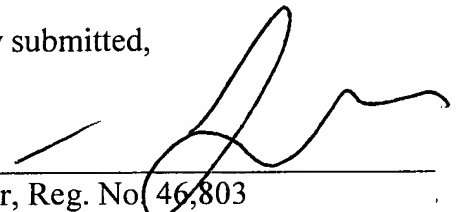
In accordance with Rules 56, 97, and 98 of the Rules of Practice in Patent Cases (37 C.F.R. §§ 1.56, 1.97, and 1.98), Form PTO/SB/08B and a copy of the reference cited therein is submitted for consideration by the Examiner. While the reference provided in this Information Disclosure Statement may be material to patentability pursuant to 37 C.F.R. § 1.56, it is not intended to constitute an admission that any reference referred to herein is prior art for this invention unless specially designated as such. Also, in accordance with 37 C.F.R. § 1.97(g), the

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filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 C.F.R. § 1.56(a) exists.

This Information Disclosure Statement is being filed before the issuance of a first office action on the merits of the application (37 C.F.R. 1.97(b)(3)); therefore, no fee is believed to be due. However, if any fee is due, the Commissioner is authorized to charge any such fee and any additional fees due or credit any overpayment to Deposit Account No. 50-0951.

Respectfully submitted,



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Docket No. 5853-268

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